



# Overlapping Protection Domains (OPD)

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## Highest Data Reliability

### Industry Challenge

Maintaining the highest level of data integrity is a challenge faced by enterprise IT managers. Providing accurate data in a timely manner gives business operations a competitive edge over their competition. In a Storage Area Network (SAN) infrastructure, Fibre Channel (FC) Host Bus Adapters (HBAs) provide critical data protection as data travels between the storage devices and the servers. To provide the highest level of data protection, IT professionals need FC HBAs that provide continuous data protection at all times.

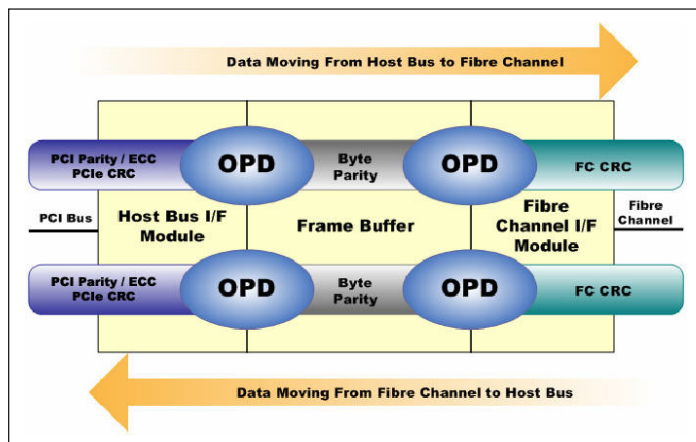
**User Benefits**

- **Highest Data Reliability**  
Data and address paths are never unprotected, even for the smallest fraction of time.

**QLogic's OPD Solution**

OPD is QLogic's unique implementation for protecting the data and address paths as this information moves between the host bus interface and the FC interface within the FC controller (ASIC). OPD on the data path was first introduced with QLogic's 4Gb ASICs and HBAs. With the introduction of QLogic's 8Gb ASICs and HBAs, OPD has been enhanced to protect both the data path *and* the control path.

QLogic's enhancement of OPD not only ensures that the correct data is written, but that the data is written to the correct location.



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**How QLogic's OPD Solution Works**

Data received from the host bus is protected by PCI parity/ECC or PCIe CRC. Before data is put into the frame buffer, frame buffer byte parity is calculated on the data. Then PCI parity/ECC or PCIe CRC is checked; if its integrity is verified, it is stripped. Similarly, when data is put on the FC interface, FC CRC is calculated on the data. The data is checked against the frame buffer parity; if its integrity is verified, it is stripped.



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